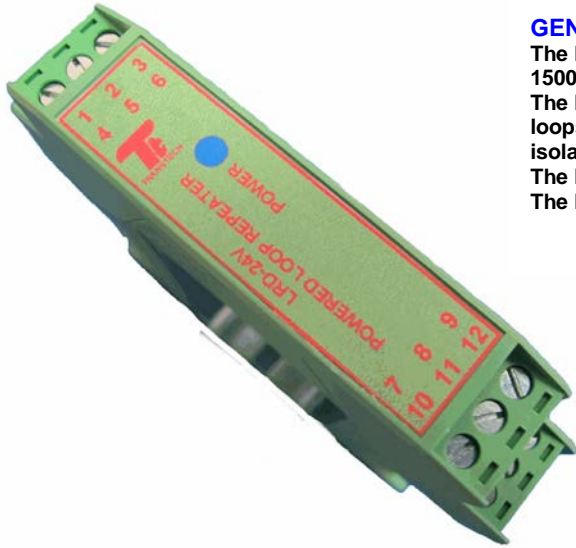


## MODEL:- LRD-24V

24V Powered Loop Repeater 2 Channel



### GENERAL DESCRIPTION

The LRD-24V is a “Universal” powered, five port isolated, loop repeater that offers up to 1500V isolation between ports. The input power for the LRD-24V is not polarity sensitive. The LRD-24V can be used as a noise filter or reducer of common mode noise in some current loops. Further, the LRD-24V may be linked on its input to give a single input and fully isolated dual output device. The LRD-24V output currents are independent of the loop resistance up to the 1200ohm limit. The LRD-24V is factory calibrated and there are no user accessible adjustments required.

### FEATURES

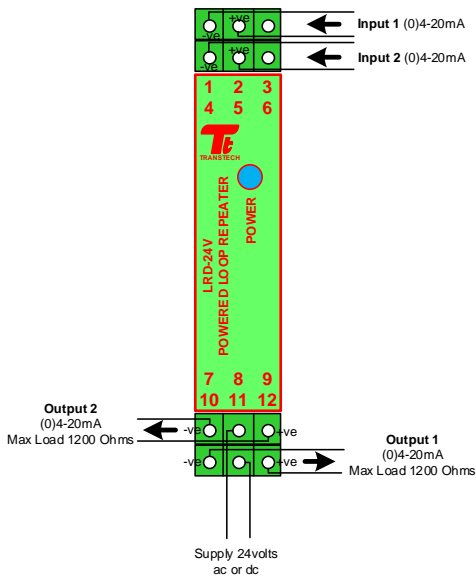
- ♦ Wide operating supply range 10-72V
- ♦ True five (5) port isolation
- ♦ Common 0V's have no effect (including Supply V)
- ♦ Port-port creepage distance ≥ 2.5mm
- ♦ No user adjustment required.
- ♦ Optical Isolation
- ♦ In excess of 1200 ohms loop resistance
- ♦ DIN rail mounting
- ♦ Very Small footprint of 17.5mm width

### TECHNICAL DATA

<b>Supply Voltage</b>	24Vac/dc nominal (10V to 72V ac or dc)
<b>Supply Power</b>	650mW plus loop power
<b>Five Port Isolation</b>	1500V nominal
<b>Input Range 2 channel</b>	0(4) to 20mA
<b>Optional Single Input</b>	Link terminals 1 & 5
<b>Output Range 2 channel</b>	0(4) to 20mA
<b>Loop Resistance</b>	1200 ohms/loop maximum
<b>ESD Protection</b>	Nominally 15kV (all ports) (IEC 61000-4-2)
<b>Power Indicator</b>	Blue LED

### GENERAL SPECIFICATION

Supply Voltage	10 to 72Vdc
Loop Resistance	1200 ohms
Long term drift	< 0.1% of span per 10,000hrs
Isolation Level	1500V d.c.
Creepage distance	≥2.5mm. (port to port)
Output Noise	3.2µA r.m.s. (typical).
Accuracy	0.1% of span
Linearity	0.1% of span
Repeatability	0.1% over 10,000hrs
CMR (50/60Hz)	Typically 110dB
Response time (to 1%)	Nominally 100mS (10 to 90%)
Operating Temp	-25 to 75 Deg. C
Storage Temp	-55 to 85 Deg. C
Terminals	Self Opening 2.5mm <sup>2</sup>
Housing Material	KRILEN
Dimensions	79mm X 106mm X 25mm
Weight	135 grams



**SCHEMATIC DIAGRAM**

